

版本 Version: V1.0

日期 Date: 2015.10.23

名称: Zigbee 模块

Name: Zigbee Module

型号 : CDT-ZBB2530-01

Model: CDT-ZBB2530-01

软件:

Software:

| 客 户<br>CUSTOMER | 客户承认<br>APPROVE (请盖印章) | 日 期<br>DATE |
|-----------------|------------------------|-------------|
|                 |                        |             |

深圳市中龙通电子科技有限公司

CHINA DRAGON TECHNOLOGY LIMITED

工厂地址: 深圳市宝安区沙井街道南浦路林坡坑蚝三第一工业园 B4 栋

电话: (86 755) 81449957

传真: (86 755) 81449967

E-mail: [Info@cdtech.com](mailto:Info@cdtech.com)[Http://www.cdtech.cn](http://www.cdtech.cn)

技术支持热线: 13902924823

客服专员: 13823632451

DESIGN: \_\_\_\_\_

CHECK: \_\_\_\_\_

APPROVAL: \_\_\_\_\_

# **CHINA DRAGON Technology**

**CDT-ZBB2530-01  
Zigbee Module**

**IEEE 802.11.15.4**

## Revision History

| Date       | Revision Content | Revised By | Version |
|------------|------------------|------------|---------|
| 2015.10.23 | First release    |            | 1.0     |
|            |                  |            |         |
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## Table of Contents

|                                       |          |
|---------------------------------------|----------|
| <b>1. Revision History</b> .....      | <b>3</b> |
| <b>2. Table of Contents</b> .....     | <b>4</b> |
| <b>3. Introduction</b> .....          | <b>5</b> |
| <b>4. General Descriptions</b> .....  | <b>5</b> |
| <b>5. Features</b> .....              | <b>5</b> |
| <b>6. Applications</b> .....          | <b>6</b> |
| <b>7. CC253X Block Diagram</b> .....  | <b>6</b> |
| <b>8. General Specification</b> ..... | <b>7</b> |
| <b>9. Pin assignments</b> .....       | <b>8</b> |
| <b>10. Application Notes</b> .....    | <b>9</b> |

## 1. Introduction

The CDT-ZBB2530-01 module provides solutions for a wide range of applications. In order to help the user to develop these applications, In order to help the user to develop these applications, this specification focuses on the usage of the module CDT-ZBB2530-01. For detailed device descriptions, complete feature lists, and performance numbers the reader is referred to the module-specific data sheet

## 2. General Descriptions

The CDT-ZBB2530-01 is a true module solution for IEEE 802.15.4, Zigbee and RF4CE applications. It enables robust network nodes to be built with very low total bill-of-material costs.

The CDT-ZBB2530-01 combines the excellent performance of a leading RF transceiver with an industry-standard enhanced 8051 MCU, in-system programmable flash memory, 8-KB RAM, and many other powerful features.

The CDT-ZBB2530-01 has various operating modes, making it highly suited for systems where ultralow power consumption is required. Short transition times between operating modes further ensure low energy consumption

## 3. Features

### • RF/Layout

- 2.4-GHz IEEE 802.15.4 Compliant RF Transceiver
- Excellent Receiver Sensitivity and Robustness to Interference
- Programmable Output Power Up to 3.5 dBm
- Only a Single Crystal Needed for Mesh Network Systems
- The Outline Dimension is 21mm × 11mm × 0.8mm that is very small

### • Low Power

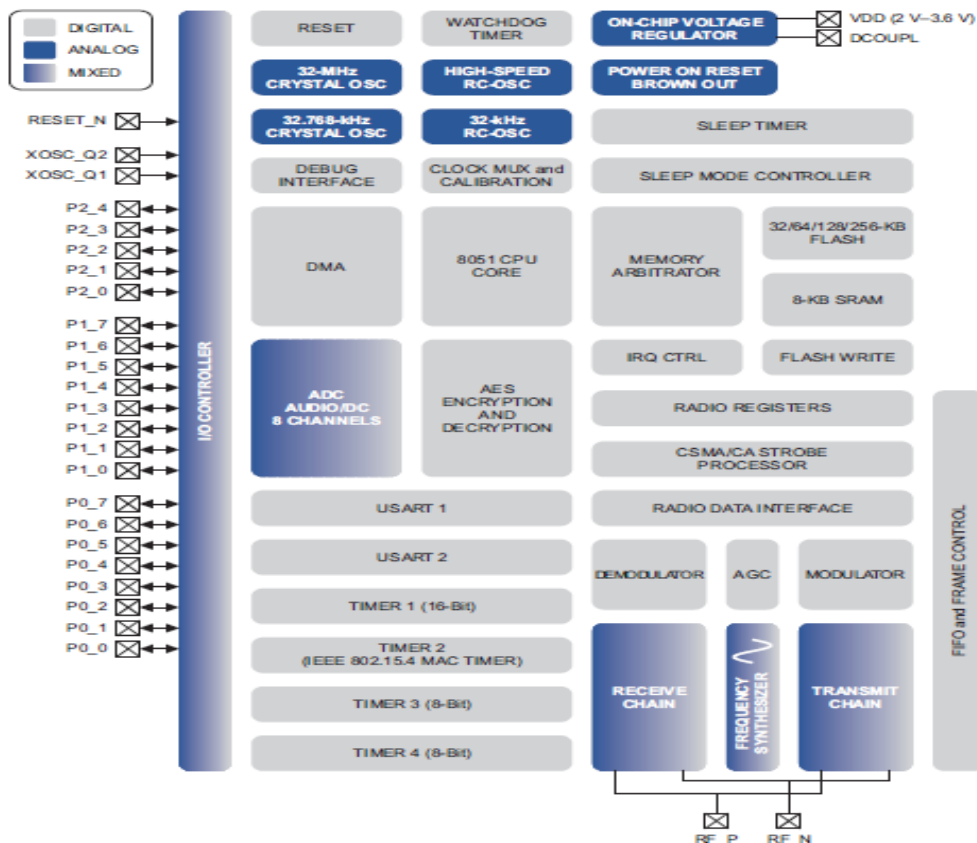
- Active-Mode RX (CPU Idle): 25 mA
- Active Mode TX at 4.5 dBm (CPU Idle): 35 mA

- Power Mode 1 (4 ms Wake-Up): 1 uA
- Power Mode 2 (Sleep Timer Running): 5 uA
- Power Mode 3 (External Interrupts): 2 uA
  
- Supply-Voltage Range (3.0 V–3.6 V)

## 4. Applications

- 2.4-GHz IEEE 802.15.4 Systems
- ZigBee Systems (256-KB Flash)
- Home/Building Automation
- Lighting Systems
- Industrial Control and Monitoring
- Low-Power Wireless Sensor Networks
- Consumer Electronics
- Health Care

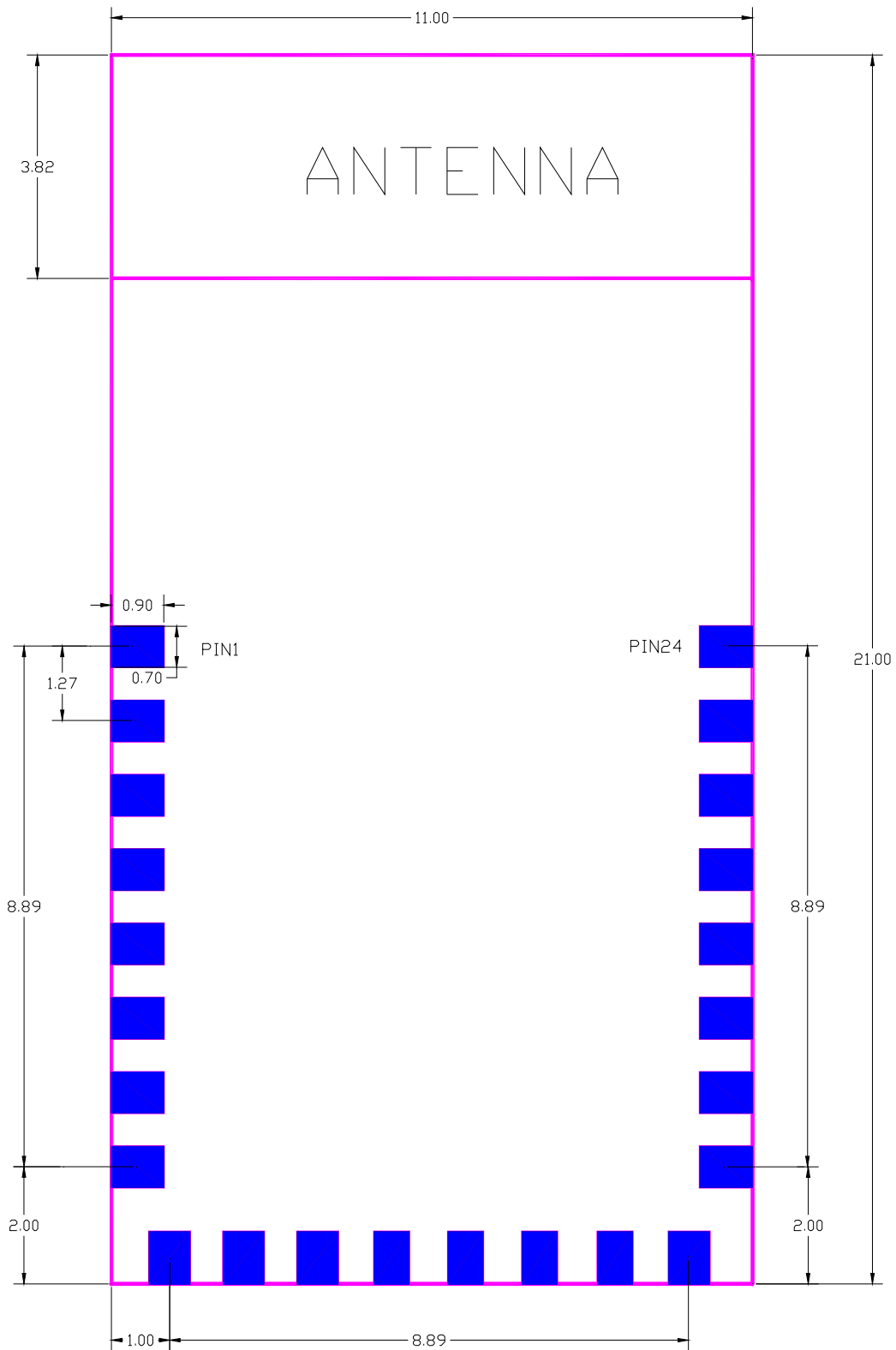
## 5. CC253X Block Diagram



## 6. General Specification

|   |   |
|---|---|
| <b>Master chip</b>  | <b>CC2530F256</b>                           |
| <b>Supply-Voltage</b>   | <b>3~3.6 V</b>                              |
| <b>RX Current</b>   | <b>25mA(type)</b>                           |
| <b>TX Current</b>   | <b>35mA(type)</b>                           |
| <b>Standby current</b>  | <b>120 uA</b>                               |
| <b>Pin Level</b>  | <b>0~3.3v</b>                               |
| <b>Frequency (Programmable )</b>  | <b>2400~2483.5 MHz</b>                      |
| <b>Frequency Offset</b>   | <b>15PPm</b>                                |
| <b>Modulation</b>   | <b>Zigbee,ZigbeePRO,ZigbeeRF4CE,6LoWPAN</b> |
| <b>Tx Power</b>   | <b>-20~+3.5 dBm</b>                         |
| <b>Rx sensitivity</b>   | <b>-95dBm</b>                               |
| <b>Communication Distance(type , Depends on the antenna and the environment</b> | <b>50~80 m</b>                              |
| <b>Operating temperature</b>  | <b>-40°C ~ +125°C</b>                       |
| <b>Outline Size</b>   | <b>21 x 11 x 0.8 mm</b>                     |
| <b>Storage Temperature</b>  | <b>-40°C ~ +125°C</b>                       |

## 7. Pin assignments





| NO | Name  | Type                             | Descriptions                         |
|----|-------|----------------------------------|--------------------------------------|
| 1  | GND   | Ground                           | Ground                               |
| 2  | VBAT  | Power                            | 3.3V digital power-supply connection |
| 3  | P2_4  | Digital, Analog I/O, XOSC32K_Q1  | 32.768 kHz XOSC                      |
| 4  | P2_3  | Digital, Analog I/O , XOSC32K_Q2 | 32.768 kHz XOSC                      |
| 5  | P2_2  | IDigital I/O                     | DC                                   |
| 6  | P2_1  | Digital I/O                      | DD                                   |
| 7  | P2_0  | Digital I/O                      | General Digital I/O                  |
| 8  | P1_7  | Digital I/O                      | General Digital I/O                  |
| 9  | GND   | Ground                           | Ground                               |
| 10 | P1_6  | Digital I/O                      | General Digital I/O                  |
| 11 | P1_5  | Digital I/O                      | General Digital I/O                  |
| 12 | P1_3  | Digital I/O                      | General Digital I/O                  |
| 13 | P1_2  | Digital I/O                      | General Digital I/O                  |
| 14 | P1_0  | Digital I/O                      | General Digital I/O                  |
| 15 | P0_7  | Digital I/O                      | General Digital I/O                  |
| 16 | P0_6  | Digital I/O                      | General Digital I/O                  |
| 17 | P0_5  | Digital I/O                      | General Digital I/O                  |
| 18 | P0_4  | Digital I/O                      | General Digital I/O                  |
| 19 | P0_3  | Digital I/O                      | UART_TX                              |
| 20 | P0_2  | Digital I/O                      | UART_RX                              |
| 21 | P0_1  | Digital I/O                      | General Digital I/O                  |
| 22 | P0_0  | Digital I/O                      | General Digital I/O                  |
| 23 | RESET | Digital input                    | Reset, active-low                    |
| 24 | GND   | Ground                           | Ground                               |

## 8. Application Notes

1. 模块下面的地尽量保持完整,天线区域下面的PCB需要镂空。
2. 供电滤波电路, 及数据线匹配电阻尽量靠近模块。
3. 模块摆放尽量远离干扰源:如WIFI天线, GSM天线, DDR CLK, LCD排线等等。